F()RM PTO-1449 (Modified)

(37 CFR § 1.98(b))

**EXAMINER:** 

U.S. Department of Commerce Patent and Trademark Office

Attorney Docket No.: SONY-14700

Serial No.: 09/904,973

TO-1449

INFORMATION DISCLOSURE STATEMENT BY APPLICANT USe Several Sheet of Necessary)

L§ 1.98(b))

U.S. PAT

Applicant: Mark K. Eyer

Filing Date: July 12, 2001

	U.S. PATENT DOCUMENTS SEP 1 2 2001						
Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Classic inology Genter 2600g Date		
B	AA	2,386,753	10/16/45	J. Shield	174	36 .	_10/03/42
<b>3</b>	AB	2,603,684	07/15/52	E.P. Holmes	174	106	07/20/48
B	AC	3,785,432	01/15/74	Kabat et al.	165	22	10/02/72
B	AD	4,376,920	03/15/83	Smith	333	12	04/01/81
M	AE	4,604,689	08/05/86	Burger	364	200	04/15/83
3	AF	4,761,519	08/02/88	Olson et al.	174	107	01/29/87
9	AG	4,763,360	08/09/88	Daniels et al.	455	3	09/17/86
B	AH	4,822,304	04/18/89	Herron	439	610	09/24/87
63	ΑI	4,842,366	06/27/89	Sawada et al.	350	96.30	03/03/88
₫1	AJ	4,853,555	08/01/89	Wheat	307	9.1	04/21/88
R	AK	4,871,883	10/03/89	Guiol	174	36	07/23/87
R	AL	4,881,244	11/14/89	Haug	375	36	12/11/87
W	AM	4,924,037	05/08/90	Ainsworth et al.	174	117	12/20/88
TD	AN	4,979,185	12/18/90	Bryans et al.	375	20	10/30/89
\$5	AO	5,055,064	10/08/91	Imaizumi et al.	439	402	02/04/91
13	AP	5,133,034	07/21/92	Arroyo et al.	385	107	08/20/91
D	AQ	5,162,609	11/10/92	Adriaenssens et al.	174	34	07/31/91
_Q	AR	5,216,202	06/01/93	Yoshida et al.	174	36	08/21/91
U	AS	5,216,204	06/01/93	Dudek et al.	174	102	08/02/91
\$	ΑT	5,244,415	09/14/93	Marsilio et al.	439	610	02/07/92
- W	AU	5,362,249	11/08/94	Carter	439	357	05/04/93
B	AV	5,400,340	03/21/95	Hillman et al.	370	105.3	03/04/93
4	AW	5,412,697	05/02/95	Van Brunt et al.	375	360	01/14/93
U	AX	5,418,478	05/23/95	Van Brunt et al.	326	86	07/30/93
ব্য	AY	5,483,656	01/09/96	Oprescu et al.	395	750	01/14/93
Q	AZ	5,485,458	01/16/96	Oprescu et al.	370	85.2	03/05/93
1	BA	5,485,488	01/16/96	Van Brunt et al.	375	257	03/29/94
47	BB	5,493,657	02/20/96	Van Brunt et al.	395	308	06/21/93
U	вс	5,499,344	03/12/96	Elnashar et al.	395	250	10/07/92
B	BD	5,500,946	03/19/96	Roden et al.	395	308	01/27/95
B	BE	5,504,458	04/02/96	Van Brunt et al. 330		255	09/30/94
T,	BF	5,504,757	04/02/96	Cook et al.	370	84	09/27/94
R	BG	5,509,126	04/16/96	Oprescu et al.	395	307	03/16/93
3	ВН	5,527,996	06/18/96	Ham	174	113 R	06/17/94
B	Ві	5,572,658	11/05/96	Mohr et al.	395	182.02	08/05/93
B	BJ	5,574,250	1/12/96	Hardie et al.	174	36	02/03/95
Examiner:			m la	Date Considered:	11,	110/05	

Initial citation considered. They one through citation if not in conformance and not considered. Include copy of this form with next communication to a second control of the conformance and not considered.

FORM PTO-1449 (Modified)

EXAMINER:

U.S. Department of Commerce Patent and Trademark Office

Attorney Docket No.: SONY-14700

Serial No.: 09/904,973

INFORMATION DISCLOSUME STATEMENT BY APPLICANT (Use Several Sheets if Necessary)

Applicant: Mark K. Eyer

(37 CFR § 1.98(b))					Filing Date: July 12, 2001		Group Art Unit:				
		<del>,</del>		U.S. PATENT DOC	UMENTS						
Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee		Class	Subclass	Filing Date			
U	BK	5,579,486	11/26/96	Oprescu et al.		395	200.15	01/14/93			
43	BL	5,592,510	01/07/97	Van Brunt et al.		375	220	03/29/94			
8	ВМ	5,619,544	04/08/97	Lewis et al.		375	377	02/27/96			
S	BN	5,754,548	05/19/98	Hoekstra et al.		370	402	02/21/97			
B	во	5,781,028	07/14/98	Decuir		326	30	06/21/96			
Ø	BP	5,796,042	08/18/98	Pope		174	102SP	06/21/96			
B	BQ	5,808,660	09/15/98	Sckine et al.		348	8	09/04/96			
A	BR	5,881,249	03/09/99	Reasoner		395	281	07/31/95			
3	BS	5,945,631	08/31/99	Henr	ikson et al.	174	34	09/16/96			
		OTHER	DOCUMENTS (Incli	uding Author, Title, D	ate, Relevant Pages, Place	of Publication)					
B	ВТ	"1394 200 Mb/s PHYsical Layer Transceiver," IBM Microelectronics, Product Data Sheet and Application Notes, Version 1.4, 3/14/96.									
R	BŲ	"IEEE 1394-1995 TRIPLE CABLE TRANSRECEIVER/ ARBITER," Texas Instruments, TSB21LV03, Product Preview, Revision 0.99, 3/19/96.									
a	BV	"P1394 Standard for a High Performance Serial Bus," IEEE P1394 Draft 8.0v2, July 7, 1995.									
JB	BW	Tensolite Company product specification, part number 20470/9J207X-4(LD).  SEP 1 2. 2001									
or	вх	Totalia Company and the conference of the confer									
J.	BÝ	Tensolite Company product specification, part number 18480/93207X-4(LD).  Tensolite Company product specification, part number 24443/9B048X-4(LD), 6/3/93.									
な	BZ	Tensolite Company product specification, part number 24443/9C062X-4(LD), 3/17/93.									
Q.	CA	Craig Theorin, "High speed serial links benefit from advanced cabling," 10/26/95.									
ΣĎ	СВ	Raychem specification control drawing, part number EPD-RWC-13458, 8/7/95.									
0	cc	Raychem specification control drawing, part number 82A0111, 9/10/95, page 1 of 2.									
B	CD	Michael Teener et al., "A Bus on a Diet - The Serial Bus Alternative, An Introduction to the P1394 High performance Serial Bus" Apple Computer, Inc. Santa Clara, CA, Pub. Date.: 02/24/92, pgs. 316-321.									
03	CE	"The IEEE-1394 High Speed Serial Bus," R.H.J. Bloks, Philips Journal Of Research, Vol.50, No. 1/2, pp. 209-216, 1996.									
S	CF	P1394a Draft Standard For A High Performance Serial Bus (Supplement), P1394a Draft 2.0 March 15, 1998.									
R	CG	Pigital Visual Interface - DVI" DDWG, Revision 1.0, April 2, 1999, page 1 of 76.									
Examiner:		17.			Date Considered:	11/10/05					

Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.